

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L13	199	wiring layer insulation solder resist	US-PGPUB; USPAT; USOCR	SAME	ON	2008/09/16 15:11
L12	36	"4763188"   "5012323"   "5025306"   "5166772"   "5200809"   "5291061"   "5323060"   "5347429"   "5365107"   "5422435"   "5463253"   "5495398"   "5502289"   "5530202"   "5656864"   "5689135"   "5696031"   "5715147"   "5721452"   "5739581"   "5793108"   "5815372"   "5866949"   "5886412"   "5973403"   "6005778"   "6051886"   "6057598"   "6072243"   "6080264"   "6133637"   "6214641"   "6246115"   "6326696"   "6429512"   "RE36613").PN.	US-PGPUB; USPAT; USOCR	SAME	ON	2008/09/16 15:06
L11	160	110 and solder resist	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2008/09/16 14:46

L10	1355	18 and interposer	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2008/09/16 14:45
L9	22	18 and interposer insulator via	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2008/09/16 14:45
L8	17672	stacked chip	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2008/09/16 14:24
S94	3168	257/686.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2008/09/16 13:33
S93	252	257/e23.048.ccls.	US-PGPUB; USPAT; USOCR	SAME	ON	2008/09/16 13:33

9/16/2008 3:55:54 PM

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 \10 529565 stacked chips with contact pillars and chip island for top chip.wsp